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Amdt. dated January 23, 2004
Reply to Offic action of Dec mber 1, 2003

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (Currently amended) A computer system, comprising:

a host computer including a CPU coupled to memory, wherein the memory stores host-specific information; and

a management device separate from and coupled to said host, said separate device requests the host's CPU to coordinate the transfer of wherein at least a portion of said host-specific information is stored in the management device during a boot process of the host computer and the management device is operable to manage a function for the host computer using the host-specific informationto the separate device prior to run-time.

2.   (Original) The computer system of claim 1 wherein said memory comprises non-volatile memory.

3.   (Original) The computer system of claim 2 wherein said memory comprises volatile memory.

4.   (Currently amended) The computer system of claim 1 wherein said separate management device comprises a subsystem of the host computerused to remotely control the host.

5.   (Currently amended) The computer system of claim 4 wherein the host specific information includes a signature which identifies the information and said separate device searches for said signature to find whereby the management device locates and transfers said host specific information.

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6.   (Canceled).

7.   (Canceled).

8.   (Currently amended) The computer system of claim 1 wherein said
separate management device includes a CPU that uses the host specific
information to control a function for the host computer.

9.   (Currently amended) The computer system of claim 1 wherein the
separate management device uploads the host specific information during a
power on self test of the host computer.

10.   (Currently amended) The computer system of claim 4 wherein said
separate management device uses said host specific information to provide
management functionality for the host computer when the host computer is in a
low power state.

11.   (Currently amended) The computer system of claim 10 wherein the host
specific information includes a signature which identifies the information and said
separate management device searches for said signature to find said host specific
information.

12.   (Canceled).

13.   (Canceled).

14.   (Currently amended) The computer system of claim 10 wherein said
separate management device includes a CPU.

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15.   (Currently amended) The computer system of claim 10 wherein said ~~separate~~management device operates from an auxiliary power source that is available even if the host computer is off.

16.   (Currently amended) The computer system of claim 10 wherein the ~~separate~~ management device uploads the host specific information during power on self test of the host.

17.   (Currently amended) A logic unit, comprising:
a CPU;
memory coupled to said CPU;
wherein said logic unit is adapted to couple to a host computer system and ~~upload~~ store a table containing host computer information in the memory during a power on self test of the host computer system whereby the logic unit uses the table to manage a function for the host computer system.

18.   (Currrently amended) The logic unit of claim 17 wherein said logic unit comprises management logic which manages a function for the~~said~~ host computer system when the host computer is in a low power state.

19.   (Currently amended) The logic unit of claim 18 wherein the host computer information ~~specific~~ includes a signature which identifies the information and said logic unit searches for said signature to find said table containing host computer ~~specific~~ information.

20.   (Currently amended) The logic unit of claim 19 wherein the logic unit is configured to request a CPU in the host computer system to coordinate the transfer of the table~~host computer specific information~~ to the logic unit.

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21.   (Currently amended) The logic unit of claim 19 wherein the logic unit uploads the ~~table~~host-computer-specific-information without the involvement of a CPU ~~in~~of the hosts computer system.

22.   (Currently amended) The logic unit of claim 17 wherein the logic unit uploads the ~~table~~host-computer-specific-information during a power on self test event as a subsystem of the host computer.

23.   (Original) The logic unit of claim 17 wherein said logic unit operates from a different power source than the host computer system and said logic unit can be powered on even if the host computer system is powered off.

24.   (Currently amended) A method of operating a logic unit coupled to a host computer, comprising:

   searching for host computer specific information during a boot process of
      the host computer;
   upon finding said information, ~~uploading~~ storing said information ~~to~~in a
      memory of the logic unit ~~with the involvement of a CPU in the host
      computer~~; and
   using the information during the operation of the logic unit to independently
      control a function for the host computer;
   wherein said searching and uploading ~~do not~~ occur ~~before~~during run-time
      of the host computer.

25.   (Currently amended) The method of claim 24 wherein searching and uploading ~~occur prior to~~ before run-time allows a CPU of the host computer to operate without interruption from the logic unit during run-time.

26.   (Canceled).

27.   (Canceled).

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28.   (New) The method of claim 24 wherein storing the computer specific information in a memory of the logic unit comprises storing at least one of an Advanced Configuration and Power Interface ("ACPI") table and a system management basic input/output system ("SMBIOS").

29.   (New) A system, comprising:
   a host computer that has a central processing unit ("CPU") coupled to a peripheral interface and a memory unit that stores an information table; and
   a management unit coupled to the peripheral interface of the host computer, the management unit accesses and stores the information table during a boot process of the host computer such that the management unit is operable to carry out a predetermined management responsibility associated with the information table prior to the host computer reaching a run-time.

30.   (New) The system of claim 29 wherein the management unit comprises a battery power supply such that the management unit is operable when the host computer is in a low power state.

31.   (New) The system of claim 29 wherein the management unit comprises:
   a ROM memory that stores computer readable instructions for accessing and storing the information table; and
   a processor that executes the computer readable instructions.

32.   (New) The system of claim 31 wherein the processor requests the CPU to transfer a copy of the information table to a memory of the management unit.

33.   (New) The system of claim 31 wherein management logic of the management unit is configured to control the host computer's peripheral interface

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and is operable to read the information table from the host computer's memory
unit such that the CPU is not needed to access and store the information table.

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